

7. (Amended) The interface circuit as claimed in claim 1, wherein respective current limiting resistors are connected upstream of the control terminals of the first and second transistors.

8. (Amended) The interface circuit as claimed in claim 1, wherein the switching point of the third switching device is adjustable by means of a voltage divider.

9. (Amended) The interface circuit as claimed in claim 1, wherein provision is made of a detection line, connected to the second input, for outputting the auxiliary supply voltage to a voltage detection device within the circuit situated on the plug-in card.

10. (Amended) The interface circuit as claimed in claim 1, wherein the switching devices have a small voltage drop in the turned-on state.

11. (Amended) The interface circuit as claimed in claim 10, wherein the switching devices have a voltage drop of less than 0.1 volt in the turned-on state.

12. (Amended) The interface circuit as claimed in claim 1, wherein the main supply voltage and the secondary supply voltage are in each case 3.1 volts to 3.5 volts.

#### REMARKS

The amendments to the specification as set forth above are intended to clarify and set apart the various sections of the subject application.

The amendments to the claims as set forth above are intended to remove all multiple dependent claims from the subject application and to more particularly point out and distinctly claim the subject invention.

Attached hereto is a marked-up version of the specification and claims 1-12, which illustrates all of the changes made to the specification and claims pursuant to 37 CFR §1.121. The attached page is captioned "Version With Markings To Show Changes Made". Deleted language is bracketed and added language is underlined.

The Commissioner is hereby authorized to charge any deficiencies or credit any overpayments in connection with the filing of this correspondence to Deposit Account No. 50-0426.

Respectfully submitted,

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**Version With Markings To Show Changes Made**

**IN THE SPECIFICATION:**

The paragraph heading has been inserted on page 1 of the English translation of the subject application, before line 5, as follows:

**Technical Field**

The paragraph heading has been inserted on page 1 of the English translation of the subject application, before line 9, as follows:

**Background Art**

The paragraph heading has been inserted on page 4 of the English translation of the subject application, before line 28, as follows:

**Summary of the Invention**

The paragraph heading has been inserted on page 7 of the English translation of the subject application, before line 5, as follows:

**Brief Description of the Drawings**

The paragraph heading has been inserted on page 7 of the English translation of the subject application, line 20, as follows:

**Detailed Description of the Invention**

**IN THE CLAIMS:**

The paragraph heading "Patent Claims" on page 14 of the English translation of the subject application has been deleted and the paragraph heading has been inserted in place thereof as follows:

**CLAIMS**

The paragraph heading has been inserted on page 14 of the English translation of the subject application, before claim 1, as follows:

**What is claimed is:**

1. (Amended) A PCI bus interface circuit for the voltage supply of a PCT plug-in card that can be connected to a PCI bus, having:

a first input [(2)] for connection to a main voltage supply line of the PCI bus;

a second input [(3)] for connection to an auxiliary voltage supply line of the PCI bus;

an output [(4)] for outputting a supply voltage to the PCI plug-in card;

a first switching device [(6)] for switching a main supply voltage that is present at the first input [(2)] to the output [(4)] if no auxiliary supply voltage  $V_{aux}$  is present at the second input [(3)];

a second switching device [(7)] for switching an auxiliary supply voltage  $V_{aux}$  that is present at the second input [(3)] to the output [(4)] if no main supply voltage  $V_{cc}$  is present at the first input [(2)] ; and having

a third switching device [(20)], which, given the simultaneous presence of a main supply voltage  $V_{cc}$  at the first input [(2)] and an auxiliary supply voltage  $V_{aux}$  at the second input [(3)], drives the second switching device [(7)] for switching the auxiliary supply voltage  $V_{aux}$  through to the output [(4)].

2. (Amended) The interface circuit as claimed in claim 1, wherein the switching devices [(6, 7, 20)] are semiconductor switches.

3. (Amended) The interface circuit as claimed in claim 1 [or 2], wherein the switching devices [(6, 7, 20)] are transistors each having a control terminal [(11, 9, 21)].

4. (Amended) The interface circuit as claimed in [one of the preceding claims] claim 1, wherein the switching devices [(6, 7, 20)] are transistors, the third switching device [(20)] being constructed complementarily with respect to the first and second switching devices [(6, 7)].

5. (Amended) The interface circuit as claimed in [one of the preceding claims] claim 3, wherein the control terminal [(11)] of the first transistor [(6)] is connected to the second input [(3)] and the control terminal [(9)] of the second transistor [(7)] is connected to the first input [(2)].

6. (Amended) The interface circuit as claimed in [one of the preceding claims] claim 3, wherein the control terminal [(21)] of the third transistor [(20)] is connected to the second input [(3)], the third transistor [(20)], when an auxiliary supply voltage is applied to the second input [(3)], turning on and connecting the control terminal [(9)] of the second transistor [(7)] to a specific voltage potential, with the results that the auxiliary supply voltage is switched through to the output [(4)].

7. (Amended) The interface circuit as claimed in [one of the preceding claims] claim 1, wherein respective current limiting resistors [(28, 27)] are connected upstream of the control terminals of the first and second transistors [(6, 7)].

8. (Amended) The interface circuit as claimed in [one of the preceding claims] claim 1, wherein the switching point of the third switching device [(20)] is adjustable by means of a voltage divider [(19, 33)].

9. (Amended) The interface circuit as claimed in [one of the preceding claims] claim 1, wherein provision is made of a detection line [(17)], connected to the second input [(3)], for outputting the auxiliary supply voltage to a voltage detection device within the circuit situated on the plug-in card.

10. (Amended) The interface circuit as claimed in [one of the preceding claims] claim 1, wherein the switching devices [(6, 7, 20)] have a small voltage drop in the turned-on state.

11. (Amended) The interface circuit as claimed in [one of the preceding claims] claim 10, wherein the switching devices [(6, 7, 20)] have a voltage drop of less than 0.1 volt in the turned-on state.

12. (Amended) The interface circuit as claimed in [one of the preceding claims] claim 1, wherein the main supply voltage and the secondary supply voltage are in each case 3.1 volts to 3.5 volts.